



SPECIFICATION

Amend paragraph beginning at page 5, line 29, as follows:

Fig. 5 shows a block diagram of a D/B converter **508**, which can be used as D/B converter **108** in system **100** according to another embodiment of the present invention. Converter **508** is similar to converter **308** (Fig. 3) and includes a wideband splitter **512**, two comparators **514a-b**, and a logic gate **516** **[[514]]**. However, one difference between converters **508** and **308**, is that, in converter **508**, each signal copy is applied to a non-inverting input of the corresponding comparator **514**. Another difference between said converters is that logic gate **516** **[[514]]** is an exclusive-NOR (XNOR) gate.